

TELETEXT RECEIVER

Patent Number: JP2000101979

Publication date: 2000-04-07

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Requested Patent: JP2000101979

Application Number: JP19980265148 19980918

Priority Number(s):

IPC Classification: H04N7/025; H04N7/03; H04N7/035; H04N5/445

EC Classification:

Equivalents:

Abstract

PROBLEM TO BE SOLVED: To simplify access to an Internet home page and to avoid the occurrence of connection malfunctions by accessing a prescribed access destination based on Internet access destination information stored in a storage means.

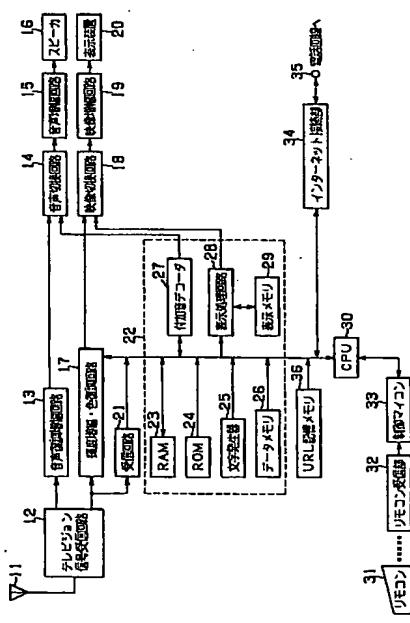
SOLUTION: Whether or not coded data of each page are configured only of an 'alphanumeric collective character code' is discriminated. The Internet addresses consist of a combination of alphanumeric characters and the URL of each Internet address is recognized, depending on whether or not the data consists of a combination of alphanumeric characters. Then the character string of the URL part in a program whose characters are recognized is stored in a URL storage memory 36, and a remote control entry terminal 31 is used to enter an access code to the Internet. A CPU 30 connects an Internet access section 34 to a telephone line and uses the URL of the Internet home page stored in the URL storage memory 36 to connect to a prescribed computer. Thus, the user can access the Internet home page without making errors in writing and memorizing the URL.

Data supplied from the esp@cenet database - I2

特許2000-101979

回路、1.8…映像切換回路、1.9…映像増幅回路、2.0…表示装置、2.1…受信回路、2.2…復号処理部、2.3…RAM、2.4…ROM、2.5…文字再生器、2.6…データメモリ、2.7…サブデコーダ、2.8…表示処理回路、2.9…表示メモリ、3.0…CPU、3.1…リモコン用入力端末、3.2…リモコン受信部、3.3…制御マイコン、3.4…インターネット接続部、3.5…接続端子、3.6…URL記憶メモリ。

11



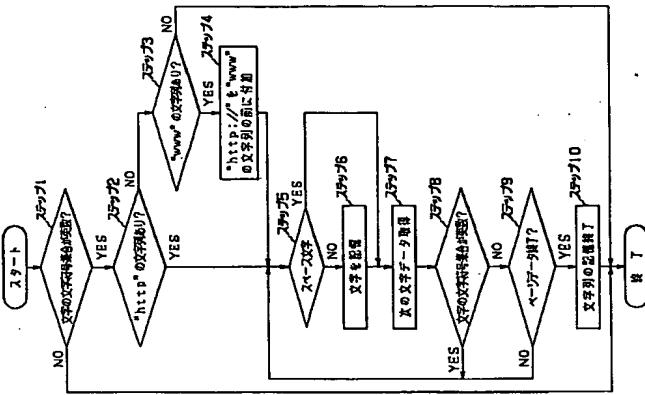
12

The screenshot shows a QR code at the top, followed by the URL <http://www.tanbin.co.jp>. Below the URL is the text 'QRコードを読み取る' (Read QR code) and 'QRコードを表示する' (Display QR code). At the bottom, there is a large red button labeled '(n)'.

【図3】

 用入力端末、3.2…リモコン受信部、3.3…制御マイコン、3.4…インターネット接続部、3.5…接続端子、3.6…URL配信メモリ。

1



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This block diagram illustrates the architecture of the Sharp X1 computer. The process starts with the **キーボード** (Keyboard) at the bottom left, which connects to the **各部制御部** (General Control Unit). This unit is connected to the **音声合成装置** (Speech Synthesis Device), **画面制御部** (Display Control Unit), and the **各部制御部** of the **内蔵装置** (Built-in Devices). The **内蔵装置** also receives input from the **各部制御部** of the **外部機器** (External Equipment). The **画面制御部** is connected to the **モニタ** (Monitor). The **各部制御部** of the **内蔵装置** is connected to the **RAM**, **ROM**, **文字送信部**, and **データモジュール**. The **RAM** and **ROM** are also connected to the **CPU**. The **文字送信部** and **データモジュール** are connected to the **各部制御部** of the **外部機器**. The **各部制御部** of the **外部機器** is connected to the **リモコン受信部** (Remote Control Receiver) and the **モニタ**. The **リモコン受信部** is connected to the **CPU**.

特開2000-101979